

CLAIMS:

1. An integrated circuit provided with at least two output drivers (4) without substrate contacts and further provided with at least a core region with a Vssc contact (7, 9) and a periphery region provided with at least one Vssq contact (8), characterized in that a resistance (11) with a value lying between 100 and 300 ohms is present between each Vssq contact (8) and the Vssc contact (7, 9).
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2. An integrated circuit as claimed in claim 1, wherein the output drivers are not slew-rate controlled, characterized in that the value of the resistance (11) is greater than 250 ohms.
- 10 3. An integrated circuit as claimed in claim 1, wherein the output drivers are slew-rate controlled, characterized in that the value of the resistance (11) is at most 250 ohms.
- 15 4. An integrated circuit as claimed in one of the claims 1 to 3, characterized in that the resistance (11) is provided in the Vssq pad.
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